

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 249437US2SRD DIV		SERIAL NO. <u>10/790,109</u> New <del>Design</del> Application	
LIST OF REFERENCES CITED BY APPLICANT				APPLICANT Takeshi UENO, et al.			
				FILING DATE <del>10/22/04</del> <u>3/2/04</u>		GROUP <u>2817</u>	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
H. c.	AA	5,963,088	10/05/1999	CZARNUL, ET AL	—	—	
H. c.	AB	5,990,737	11/23/1999	CZARNUL, ET AL	—	—	
H. c.	AC	6,329,849 B1	12/11/2001	CZARNUL, ET AL	—	—	
H. c.	AD	5,696,457	12-1997	REZZI ET AL	—	—	
H. c.	AE	5,345,190	09/1994	KAYLOR	—	—	
H. c.	AF	5,491,447	02/1996	GOETSCHER ET AL	—	—	
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION		
					YES	NO	
H. c.	AO	10-51247	02/20/1998	JAPAN		X	
H. c.	AP	11-17466	01/22/1999	JAPAN		X	
H. c.	AQ	2000-148242	05/26/2000	JAPAN		X	
	AR						
	AS						
	AT						
	AU						
	AV						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)							
H. c.	AW	Zdzislaw CZARNUL, et al., "DESIGN OF ULLY BALANCED ANALOG SYSTEMS BASED ON ORDINARY AND/OR MODIFIED SINGLE-ENDED OPAMPS", IEICE Trans. Fundamentals, Vol. E82-A, No. 2 February 1999, pgs. 256-270.					
H. c.	AX	Tetsuro ITAKURA et al., "A 2-V pp LINEAR INPUT-RANGE FULLY BALANCED CMOS TRANSDUCTOR AND ITS APPLICATION TO A 2.5 V 2.5 MHz, Gm-C LPF", IEICE Trans. Fundamentals Vol. E83-A, No. 11, November 2000, pgs. 2295-2302.					
	AY						
	AZ						
Examiner <u>Henry Choe</u>					Date Considered <u>10/8/04</u>		
<small>*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</small>							